



HE83115 APPROVAL AND TOOLING FORM

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COMPANY NAME: _____ DATE: _____
 PART NUMBER: HE83115
 PROJECT NAME: _____
 CODE NUMBER: HE83115-
 PRODUCTION NUMBER _____ (By King Billion)

PRODUCTION INFORMATION:

Package Type: <input type="checkbox"/> Package Form (_____) <input type="checkbox"/> Chip Form Ink: Line one – _____ Line two – _____ Line three – _____ Remark: _____

CODE INFORMATIN:

File Name: _____ Check Sum: _____ Object Code length: _____

DEVICE OPTION:

IC Version: A1 A2 (by King Billion)
 Operation Voltage: Two-Battery Three-Battery Other(_____)
 Mask Options:

NAME	DESCRIPTION	Mask Option
MO_PORE	Internal Power On Reset	<input type="checkbox"/> Disable(0) <input type="checkbox"/> Enable(1)
MO_FCK MO_SCKN	Clock Mode Select	<input type="checkbox"/> Dual Clock (10) <input type="checkbox"/> Fast Only (11) <input type="checkbox"/> Slow Only (00)
MO_FXTAL	Osc. Type of Fast Clock	<input type="checkbox"/> RC(0) <input type="checkbox"/> X'tal(1)
MO_SXTAL	Osc. Type of Slow Clock	<input type="checkbox"/> RC(0) <input type="checkbox"/> X'tal(1)
MO_WDTE	Watch Dog Timer	<input type="checkbox"/> Disable(0) <input type="checkbox"/> Enable(1)
MO_FOSCE	Fast Clock Source Select	<input type="checkbox"/> Internal(0) <input type="checkbox"/> External(1)
MO_FRCS[2:0]	Internal Fast Clock Rate Select (If internal clock is selected.)	<input type="checkbox"/> ~ = 990KHz(000) <input type="checkbox"/> ~ = 1.1MHz(001) <input type="checkbox"/> ~ = 1.3MHz(010) <input type="checkbox"/> ~ = 1.6MHz(011) <input type="checkbox"/> ~ = 2MHz(100) <input type="checkbox"/> ~ = 2.6MHz(101) <input type="checkbox"/> ~ = 3.9MHz(110) <input type="checkbox"/> ~ = 6.5MHz(111)

NAME	DESCRIPTION	Mask Option
MO_CPP[0]	Port C Bit 0 Configuration	<input type="checkbox"/> Open-drain <input type="checkbox"/> Push-pull
MO_CPP[1]	Port C Bit 1 Configuration	<input type="checkbox"/> Open-drain <input type="checkbox"/> Push-pull
MO_CPP[2]	Port C Bit 2 Configuration	<input type="checkbox"/> Open-drain <input type="checkbox"/> Push-pull
MO_CPP[3]	Port C Bit 3 Configuration	<input type="checkbox"/> Open-drain <input type="checkbox"/> Push-pull
MO_DPP[0]	Port D Bit 0 Configuration	<input type="checkbox"/> Open-drain <input type="checkbox"/> Push-pull
MO_DPP[1]	Port D Bit 1 Configuration	<input type="checkbox"/> Open-drain <input type="checkbox"/> Push-pull
MO_DPP[2]	Port D Bit 2 Configuration	<input type="checkbox"/> Open-drain <input type="checkbox"/> Push-pull
MO_DPP[3]	Port D Bit 3 Configuration	<input type="checkbox"/> Open-drain <input type="checkbox"/> Push-pull
MO_DPP[4]	Port D Bit 4 Configuration	<input type="checkbox"/> Open-drain <input type="checkbox"/> Push-pull
MO_DPP[5]	Port D Bit 5 Configuration	<input type="checkbox"/> Open-drain <input type="checkbox"/> Push-pull
MO_DPP[6]	Port D Bit 6 Configuration	<input type="checkbox"/> Open-drain <input type="checkbox"/> Push-pull
MO_DPP[7]	Port D Bit 7 Configuration	<input type="checkbox"/> Open-drain <input type="checkbox"/> Push-pull
MO_14PP[0]	Port 14 Bit 0 Configuration	<input type="checkbox"/> Open-drain <input type="checkbox"/> Push-pull
MO_14PP[1]	Port 14 Bit 1 Configuration	<input type="checkbox"/> Open-drain <input type="checkbox"/> Push-pull
MO_14PP[2]	Port 14 Bit 2 Configuration	<input type="checkbox"/> Open-drain <input type="checkbox"/> Push-pull
MO_14PP[3]	Port 14 Bit 3 Configuration	<input type="checkbox"/> Open-drain <input type="checkbox"/> Push-pull
MO_14PP[4]	Port 14 Bit 4 Configuration	<input type="checkbox"/> Open-drain <input type="checkbox"/> Push-pull
MO_14PP[5]	Port 14 Bit 5 Configuration	<input type="checkbox"/> Open-drain <input type="checkbox"/> Push-pull
MO_14PP[6]	Port 14 Bit 6 Configuration	<input type="checkbox"/> Open-drain <input type="checkbox"/> Push-pull
MO_14PP[7]	Port 14 Bit 7 Configuration	<input type="checkbox"/> Open-drain <input type="checkbox"/> Push-pull
MO_LIO14[0]	Port 14 Bit 0 I/O or LCD bit	<input type="checkbox"/> I/O <input type="checkbox"/> LCD
MO_LIO14[1]	Port 14 Bit 1 I/O or LCD bit	<input type="checkbox"/> I/O <input type="checkbox"/> LCD
MO_LIO14[2]	Port 14 Bit 2 I/O or LCD bit	<input type="checkbox"/> I/O <input type="checkbox"/> LCD
MO_LIO14[3]	Port 14 Bit 3 I/O or LCD bit	<input type="checkbox"/> I/O <input type="checkbox"/> LCD
MO_LIO14[4]	Port 14 Bit 4 I/O or LCD bit	<input type="checkbox"/> I/O <input type="checkbox"/> LCD
MO_LIO14[5]	Port 14 Bit 5 I/O or LCD bit	<input type="checkbox"/> I/O <input type="checkbox"/> LCD
MO_LIO14[6]	Port 14 Bit 6 I/O or LCD bit	<input type="checkbox"/> I/O <input type="checkbox"/> LCD
MO_LIO14[7]	Port 14 Bit 7 I/O or LCD bit	<input type="checkbox"/> I/O <input type="checkbox"/> LCD

NOTE :對於使用 TP 自動加 1 的功能，因為在 C 版 ICE 無法模擬到此現象，因此使用者需小心使用，或是使用 V3.0 的 ICE 即可解決此問題。

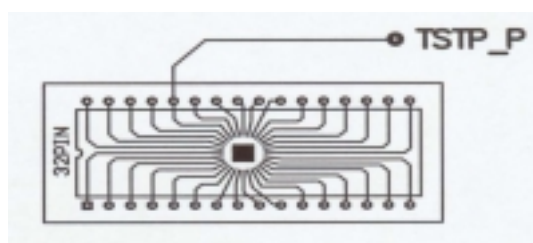
NOTE:

LCD driving circuit must be turn off before IC goes into sleep mode.

Please bonds the TSTP_P with test point on PCB (can be soldered and probed) as you can, then KB can do some IC testing job on PCB.

Neither VDD nor GND connection is necessary for TSTP_P.

The following figure is an example (Testing point with through hole).





DEVICE USAGE CHECK: (for double check purpose only)

Clock Mode:	<input type="checkbox"/> Dual	<input type="checkbox"/> Fast	<input type="checkbox"/> Slow	<input type="checkbox"/> Idle	<input type="checkbox"/> Sleep
Reset Usage:	<input type="checkbox"/> External	<input type="checkbox"/> Internal			
Watch Dog Timer Usage:	<input type="checkbox"/> WDT				
I/O Usage:	_____ Input	_____ Output	_____ Bidirectional		
RAM Usage:	Total _____ Byte is used.				
ROM Usage:	Total _____ KB is used, _____ KB is utilized as program ROM.				
Timer Usage:	<input type="checkbox"/> Timer I	<input type="checkbox"/> Timer II			
LCD Usage:	_____ COM,	_____ SEG			
Speech Usage:	<input type="checkbox"/> PWM Output	<input type="checkbox"/> D/A Output			

APPROVED BY: ICE ROMLESS DEMOBOARD OTHER(_____)

COMMENT:

CUSTOMER APPROVAL BY:

SIGNATURE:

PRINTED NAME:

TITLE:

K.B. CONFIRMATION BY:

SIGNATURE:

PRINTED NAME:

TITLE: